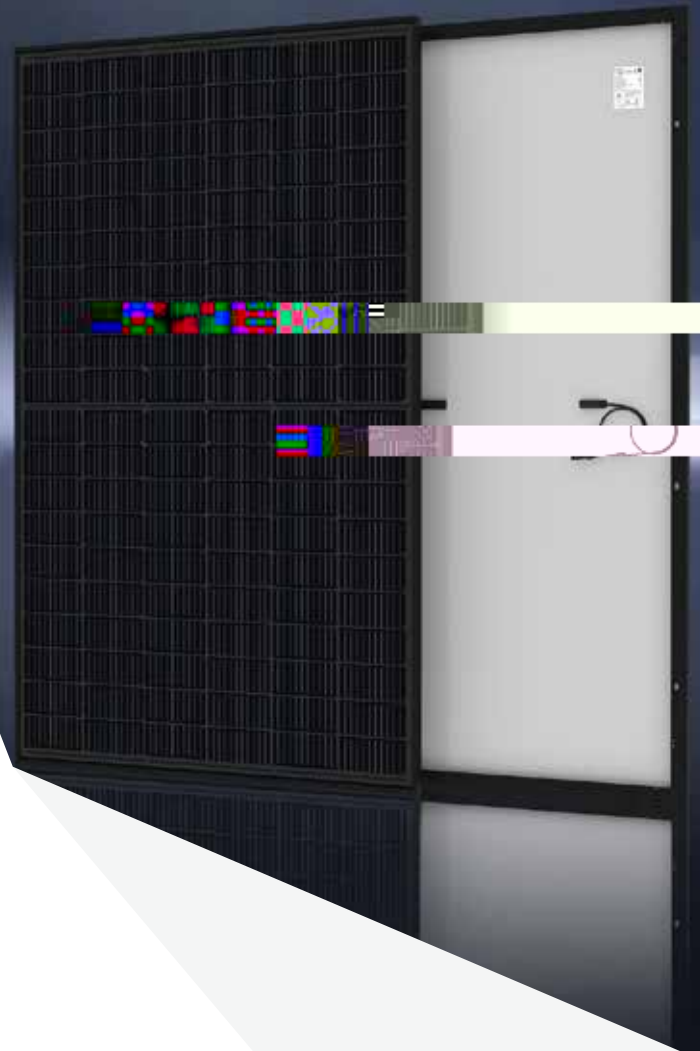


# BISTAR



## 10BB Half-cut Cell Technology

New circuit design, lower internal current, lower  $R_s$  loss  
Ga doped wafer, attenuation <2% (1st year) / 0.55% (Linear)



## Significantly Lower the Risk of Hot Spot

Special circuit design with much lower hot spot temperature



## Lower LCOE

2% more power generation, lower LCOE



## Excellent Anti-PID Performance

2 times of industry standard Anti-PID test



## IP68 Junction Box

High waterproof level



12 Years  
Quality  
Assurance

2 Years  
Power Output  
Guarantee

Linear Performance Warranty

